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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/772,113 02/03/2004		Mehran Mokhtari	B-5100 620982-2	4865	
36716 7	7590 02/08/2005		EXAMINER		
LADAS & PARRY 5670 WILSHIRE BOULEVARD, SUITE 2100			JEAN PIERRE, PEGUY		
	ES, CA 90036-5679	115 2100	ART UNIT	PAPER NUMBER	
	,		4010		

DATE MAILED: 02/08/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

		Applic	ation No.	Applicant(s)					
			2,113	MOKHTARI ET AL.					
	Office Action Summary	Exami	ner	Art Unit					
			JeanPierre	2819					
Period fe	The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply								
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).									
Status									
1)🖂	Responsive to communication(s) filed on 03 February 2004.								
2a)□		2b)⊠ This action i							
3)□									
	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.								
Disposit	ion of Claims								
4)⊠	Claim(s) 1-14 is/are pending in the	application.		•					
,	4a) Of the above claim(s) is/are withdrawn from consideration.								
5)□	Claim(s) is/are allowed.								
· —	· <u> </u>								
·	7)⊠ Claim(s) <u>4-8 and 12-14</u> is/are objected to.								
•	8) Claim(s) are subject to restriction and/or election requirement.								
Applicat	ion Papers								
9)[]	The specification is objected to by th	e Examiner.	•						
•	10)⊠ The drawing(s) filed on <u>03 February 2004</u> is/are: a)⊠ accepted or b)□ objected to by the Examiner.								
يكره.	Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).								
	Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).								
11)	The oath or declaration is objected to								
Priority under 35 U.S.C. § 119									
-	-	for foreign priority	undor 35 I I S C & 110/a) (d) or (f)					
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 									
2. Certified copies of the priority documents have been received in Application No									
3. Copies of the certified copies of the priority documents have been received in this National Stage									
	application from the International Bureau (PCT Rule 17.2(a)).								
* See the attached detailed Office action for a list of the certified copies not received.									
Aug-t-	.4(-)								
Attachment(s) 1) Notice of References Cited (PTO-892) 4) Interview Summary (PTO-413)									
	ce of References Cited (P10-692) ce of Draftsperson's Patent Drawing Review (F	PTO-948)	Paper No(s)/Mail D	ate					
3) Infor	rmation Disclosure Statement(s) (PTO-1449 or er No(s)/Mail Date		5) Notice of Informal F 6) Other:	Patent Application (PTO-152)					

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DETAILED ACTION

Claim Rejections - 35 USC § 112

1. Claims 2-8 and 13-14 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

In claim 2, line 5, the term "the electrical signal" is confusing. It seems to refer to the input voltage of electrical signal or the reference voltage of the electrical signal; in claim 3, line 1, the term a detection signal is confusing. It seems to refer to the detection signal previously recited in claim 1, line 6.;

in claims 13 and 14,line 3, the term "applying a supply voltage" is confusing. It seems to refer to the supply voltage previously recited in claim 12, line 1 (page 22).

Claim Rejections - 35 USC § 103

- 2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 3. Claims 1-3 are rejected under 35 U.S.C. 103(a) as being unpatentable over Seki et al.(USP 5,831,567) in view of Kimura et al.(USP 6,281,828).

Kimura et al. disclose in Figure 1, an analog to digital converter that comprises a comparator section that comprises a plurality of comparators (9) that determines (outputs a detection level) a relationship in level between an input analog signal (1) and a reference signal (3), an encoder (5) that converts the comparison result of the

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comparator into a digital signal (see col. 2, lines 28-33).

Kimura et al. fail to teach a plurality of reference circuits where each reference circuit is coupled to a respective one of the plurality of comparators.

Seki et al. disclose in Figure 8 an analog to digital converter that comprises a plurality of comparators (12¹⁻⁵); a plurality of reference circuits; each comparator compares an input electrical signal (10) with a respective pre-selected reference voltage (11¹⁻⁵) (see col. 6, lines 13-16) to eliminate DC offset which is contained in the analog signal. Therefore, it would have been obvious to one having ordinary skill in the art to modify the reference voltage generator of Kimura et al. by coupling individual reference circuit having pre-selected reference voltage to each comparator as taught by Seki et al. to improve performance and accuracy of the converter.

Claim Rejections - 35 USC § 102

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 5. Claims 9-11 are rejected under 35 U.S.C. 102(b) as being anticipated by Seki et al. (USP 5,831,567).

Seki et al. disclose in Figure 8 an analog to digital converter that comprises a plurality of comparators (12¹⁻⁵); a plurality of reference circuits; each comparator compares an input electrical signal (10) with a respective pre-selected reference voltage (11¹⁻⁵) (see

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col. 6, lines 13-16) to generate a detection signal that is indicative of the input electrical signal.

Allowable Subject Matter

6. Claims 4-8 and 12-14 would be allowable if rewritten to overcome the rejection(s) under 35 U.S.C. 112, 2nd paragraph, set forth in this Office action and to include all of the limitations of the base claim and any intervening claims.

Conclusion

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Peguy JeanPierre whose telephone number is (571) 272-1803. The examiner fax phone number is (571) 273-1803.

Peguy JeanPierre Primary Examiner